

APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Type:: REGULAR
Subject Matter:: UTILITY
CD-ROM or CD-R?: NONE
Title:: SEMICONDUCTOR INTEGRATED
CIRCUIT HAVING LATCHING MEANS
CAPABLE OF SCANNING
Attorney Docket Number:: 242278US-620-39-2-S DIV
Total Drawing Sheets:: 6

INVENTOR INFORMATION

Applicant Authority Type:: INVENTOR
Primary Citizenship Country:: Japan
Status:: FULL CAPACITY
Given Name:: Ryo
Family Name:: FUKUDA
City of Residence:: Yokohama-shi
Country of Residence:: Japan
Street of Mailing Address:: c/o Intellectual Property Division,
KABUSHIKI KAISHA TOSHIBA, 1-1
Shibaura 1-chome, Minato-ku, Tokyo
Country of Mailing Address:: Japan
Postal or Zip Code of Mailing Address:: 105-8001

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Division of	09/993,603	11/27/01

FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2001-304750	Japan	09/28/01	YES